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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Applicant: LIU et al. Docket No: 16405-0013  
Serial No: 09/827,056 Group Art Unit: 2811  
Filing Date: April 3, 2001 Examiner: Tran, T.F.  
For: METHOD OF FABRICATING HIGH-COUPLING RATIO SPLIT-GATE  
FLASH MEMORY CELL ARRAY

Commissioner of Patents and Trademarks  
Washington, D.C. 20231

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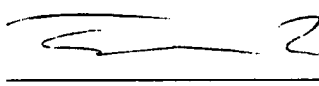
PETITION FOR EXTENSION OF TIME

Applicants hereby petitions for a one (1) month extension of time to reply to the Restriction Requirement of January 4, 2002. Applicants encloses a check to cover the amount of \$110.00 for the 2 month extension fee. A response to the Restriction Requirement is being filed.

The Commissioner is authorized to charge any required fees, or credit any overpayment to Deposit Account No. 02-3964.

Respectfully submitted,

Dated: March 4, 2002

  
Tamiz Khan  
Reg. No. 46,273

OPPENHEIMER WOLFF & DONNELLY LLP  
Customer No. 25696

**CERTIFICATE OF MAILING (37 CFR 1.10(a))**

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited on March 4, 2002, with the U.S. Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C., 20231.

Date: March 4, 2002

  
Yolette Yturralde-Owen

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